

**AMENDMENTS**

**In the Claims:**

1. (Currently Amended) A testing method for testing contacting between a semiconductor device and a carrier, comprising loading the carrier with the semiconductor device, such that contacting between the carrier and the semiconductor device is tested immediately after the loading of the carrier with the semiconductor device, wherein the semiconductor device comprises one or more contacts for testing the contacting between the semiconductor device and the carrier.

2. (Previously Presented) The testing method according to claim 1, further comprising connecting the carrier to a testing apparatus.

3. (Previously Presented) The testing method according to claim 2, wherein the carrier is connected to the testing apparatus, and the carrier is subsequently loaded with the semiconductor device.

4. (Previously Presented) The testing method according to claim 1, wherein the carrier is loaded at a carrier loading station, and the contacting between the carrier and the semiconductor device is tested before the carrier is transported to a further station.

5. (Previously Presented) The testing method according to claim 2, wherein the contacting between the carrier and the semiconductor device is tested by the testing apparatus.

6. (Previously Presented) The testing method according to claim 5, wherein the testing apparatus is configured such that it tests the contacting between the carrier and the semiconductor device, but not functioning of the semiconductor device.

7. (Previously Presented) The testing method according to claim 1, wherein the contacting between the carrier and the semiconductor device is tested less than 2 seconds after loading of the carrier with the semiconductor device.

8. (Previously Presented) The testing method according to claim 1, further comprising determining during the testing of the contacting between the carrier and the semiconductor device whether an electric contact has been established between a corresponding pad of the semiconductor device and an assigned pad of the carrier after loading of the carrier with the semiconductor device.

9. (Previously Presented) The testing method according to claim 1, further comprising determining during the testing of the contacting between the carrier and the semiconductor device whether a respective electric contact has been established between a plurality of pads of the semiconductor device and respectively assigned pads of the carrier after loading of the carrier with the semiconductor device.

10. (Previously Presented) The testing method according to claim 8, wherein power of current flowing through the corresponding semiconductor device pad is determined to find whether an electric contact has been established between a corresponding pad of the semiconductor device and an assigned pad of the carrier.

11. (Previously Presented) The testing method according to claim 8, wherein an amount of voltage dropping across the corresponding semiconductor device pad is determined to find whether an electric contact has been established between a corresponding pad of the semiconductor device and an assigned pad of the carrier.

12. (Currently Amended) A testing system for testing contacting between a semiconductor device and a carrier, comprising:

a testing apparatus to which a carrier can be connected, and which is configured such that contacting between the carrier and the semiconductor device is tested by the testing device immediately after loading of the carrier with a semiconductor device; and

one or more contacts to test the contacting between the semiconductor device and the carrier.

13. (Previously Presented) The testing system according to claim 12, wherein the testing apparatus performs the test after a signal is output by a loading device, the signal indicating that the carrier was loaded with the semiconductor device.

14. (Previously Presented) The testing system according to claim 12, further comprising a testing apparatus, the testing apparatus being configured such that contacting between the carrier and the semiconductor device is tested immediately after loading of the carrier with the semiconductor device.